Electronic Filing System (EFS) Data Electronic Patent Application Submission USPTO Use Only

EFS ID:

65471

Application ID:

10710692

Title of Invention:

Implemented Using Low Voltage

Transistors

First Named Inventor:

Sanish JACOB

Domestic/Foreign Application:

Domestic Application

High Voltage Circuits

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PATENT	APPLICATION	SERIAL	NO.	·	

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01 FC:1001 770.00 DA 02 FC:1201 774.00 DA 03 FC:1202 1296.00 DA

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